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## **Booth & Wright, L.L.P.**

Attorneys at Law PO Box 50010 Austin, TX 78763-0100 Telephone: 512.474.8488

Fax: 512.474.7996

Date: March 4, 2001

To: U.S. Patent & Trademark Office

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Karen S. Wright

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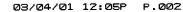
Pages (including cover): 2

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512-474-7996











## United States Patent and Trademark Office

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/710,057	11/10/2000	2872	0	62061.0105	5	30	10

23309 **BOOTH & WRIGHT LLP** P O BOX 50010 AUSTIN, TX 78763-0010 **FILING RECEIPT** OC00000005783660

Date Mailed: 02/21/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Becky Cavanaugh, Austin, TX; Robert Douglas Gowin JR., Austin, TX; Eric T. Hennenhoefer, Austin, TX;

Continuing Data as Claimed by Applicant

THIS APPLN CLAIMS BENEFIT OF 60/165,204 11/12/1999

Foreign Applications

If Required, Foreign Filing License Granted 02/20/2001

LEASE CORRECT - PTO Error

Title

Obsidian software, Inc. Method and Apparatus that Simulates the Execution of Parallel Instructions in Processor Functional Verification Testing

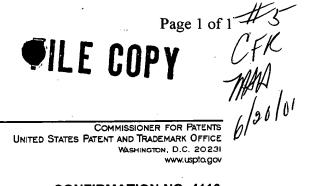
**Preliminary Class** 359

Data entry by : MONROE, BEULAH

Team: OIPE

Date: 02/21/2001

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## UNITED STATES PATENT AND TRADEMARK OFFICE





Bib Data Sheet

**CONFIRMATION NO. 4116** 

SERIAL NUMBER 09/710,057	FILING DATE 11/10/2000 RULE	CLASS 359	GROU	JP ART UNIT 2872		ATTORNEY OCKET NO. 62061.0105					
APPLICANTS  Becky Cavanaugh, Austin, TX;  Robert Douglas Gowin JR., Austin, TX;  Eric T. Hennenhoefer, Austin, TX;											
** CONTINUING DATA **********************************											
** FOREIGN APPLICATIONS ************************************											
Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged Æx	STATE OF COUNTRY		VING CLA	IMS	INDEPENDENT CLAIMS 10						
Acknowledged Examiner's Signature Initials  ADDRESS 23309											
TITLE  Method and apparatus that simulates the execution of paralled instructions in processor functional verifacation testing											
			:	All Fees							
				1.16 Fees (Filing)							
FILING FEE FEE	S: Authority has been (	☐ 1.17 Fees ( Processing Ext. of time )									
790 No.	for following	g:	1.18 Fees (Issue)								
				Other							
	_		☐ Credit								